[Name of the Document] Abstract

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[Object] A parallel processor that performs efficient parallel processing is provided.

5   [Solution Means] The parallel processor, which performs parallel processing of one or more basic instructions contained in each of instruction words delimited by instruction delimiting information, includes: a plurality of instruction execution units, EU0, EU1, that

10   perform processes corresponding to supplied basic instructions in parallel; an instruction fetch unit 46 that fetches the instruction words one by one in accordance with the instruction delimiting information; and an instruction issue unit 72 that issues each of the

15   basic instructions contained in each of the instruction words fetched by the instruction fetch unit 46 to a corresponding one of the instruction execution units.

[Selected Figure] FIG. 3